

IN THE CLAIMS

Please amend the claims as follows:

1. (unchanged) A method for receiving a wireless signal by a computer adapted to operate in a power-saving mode, said method comprising the steps of:

detecting within a computer a wireless signal representing a bit sequence when said computer is operating in a power-saving mode, wherein said wireless signal is targeted for said computer;

exiting said power-saving mode automatically in response to said wireless signal;

regenerating some or all of said bit sequence of said wireless signal; and

storing said some or all of said bit sequence of said wireless signal in a memory after exiting said power-saving mode.

2. (unchanged) The method of claim 1, further includes the steps of:

determining whether a wireless signal receiver device is installed and enabled by reading a plurality of status signals; and

exiting said power-saving mode only if said wireless signal receiver device is installed and enabled.

3. (unchanged) The method of claim 1, wherein said detecting further includes detecting a particular identification tag embedded in said bit sequence.

4. (unchanged) The method of claim 1, wherein wireless signal is transmitted through a radio frequency channel.

1 5. (unchanged) The method of claim 1, wherein said bit sequence includes a request for said  
2 computer to exit said power-saving mode.

1 6. (unchanged) The method of claim 1, wherein said bit sequence includes a request to  
2 continue execution of a program that is suspended while said computer is in said power-saving  
3 mode.

1 7. (unchanged) The method of claim 1, wherein said computer comprises a receiving means  
2 for detecting said wireless signal, and said computer further comprises a switch for maintaining  
3 power to said receiving means while operating in power-saving mode, and further comprising the  
4 step of:

5 setting said switch to maintain power to said receiving means prior to entering said  
6 power-saving mode.

1 9. (unchanged) The method of claim 1, further includes the steps of:

2 processing information conveyed by said bit sequence; and

3 returning to said power-saving mode.

4 10. (Twice amended) A computer for receiving a wireless signal while in a power-saving  
5 mode, said computer comprising:

6 a receiving means adapted to detect a wireless signal representing a sequence of  
7 bits, wherein said receiving means is adapted to regenerate some or all of said bit  
8 sequence, wherein said wireless signal is targeted for said computer;

6 a power-saving mode control means adapted to exit said power-saving mode in  
7 response to a detection of said wireless signal when said computer is in said power-saving  
8 mode;

543  
10 a switch for enabling power to said receiving means when said computer is in said  
power-saving mode; and

81  
11 a memory for storing said some or all of said regenerated bit sequence after said  
12 computer has exited said power-saving mode.  
cont

1 11. (unchanged) The computer of claim 10, further includes:

2 one or more status indicators for indicating whether said receiving means is  
3 installed and enabled; and

4 wherein said power-saving mode control is adapted to exit said power-saving  
5 mode only if said one or more status indicators show that said receiving means is  
6 installed and enabled.

Please cancel Claim 13.

1 14. (unchanged) The computer of claim 10, wherein said receiving means is an optional  
2 attachment to said computer.

1 15. (unchanged) The computer of claim 10, wherein said receiving means is formed in a  
2 device bay cover.

1 16. (unchanged) The computer of claim 15, wherein said device bay cover is an optional  
2 attachment to said computer.

1 17. (Twice amended) A computer, comprising:

2 a receiving means for receiving a signal representing a bit sequence;

3 a power saving mode selection means for selectively entering and exiting a power-  
4 saving mode; and

5 a detection means within said receiving means for detecting a wireless signal  
6 targeted for said computer while said computer is in a power-saving mode; and

7 a control means within said power saving mode selection means for exiting said  
8 power-saving mode in response to said detected wireless signal.

1 18. (unchanged) The computer of claim 17, further includes

2 means for disabling at least one power source when said computer is in said  
3 power-saving mode, wherein said receiving means asserts a wake up signal to said control  
4 means to indicate said detected wireless signal is targeted for said computer; and

5 a power management circuit to enable at least one power source, in response to  
6 said asserted wake up signal.

1 19. (unchanged) The computer of claim 17, wherein said receiving means is an option card  
2 coupled to said computer through an option card bus slot.